

[illegible]

整理番号=990515

(1)

【書類名】 図面

【図1】

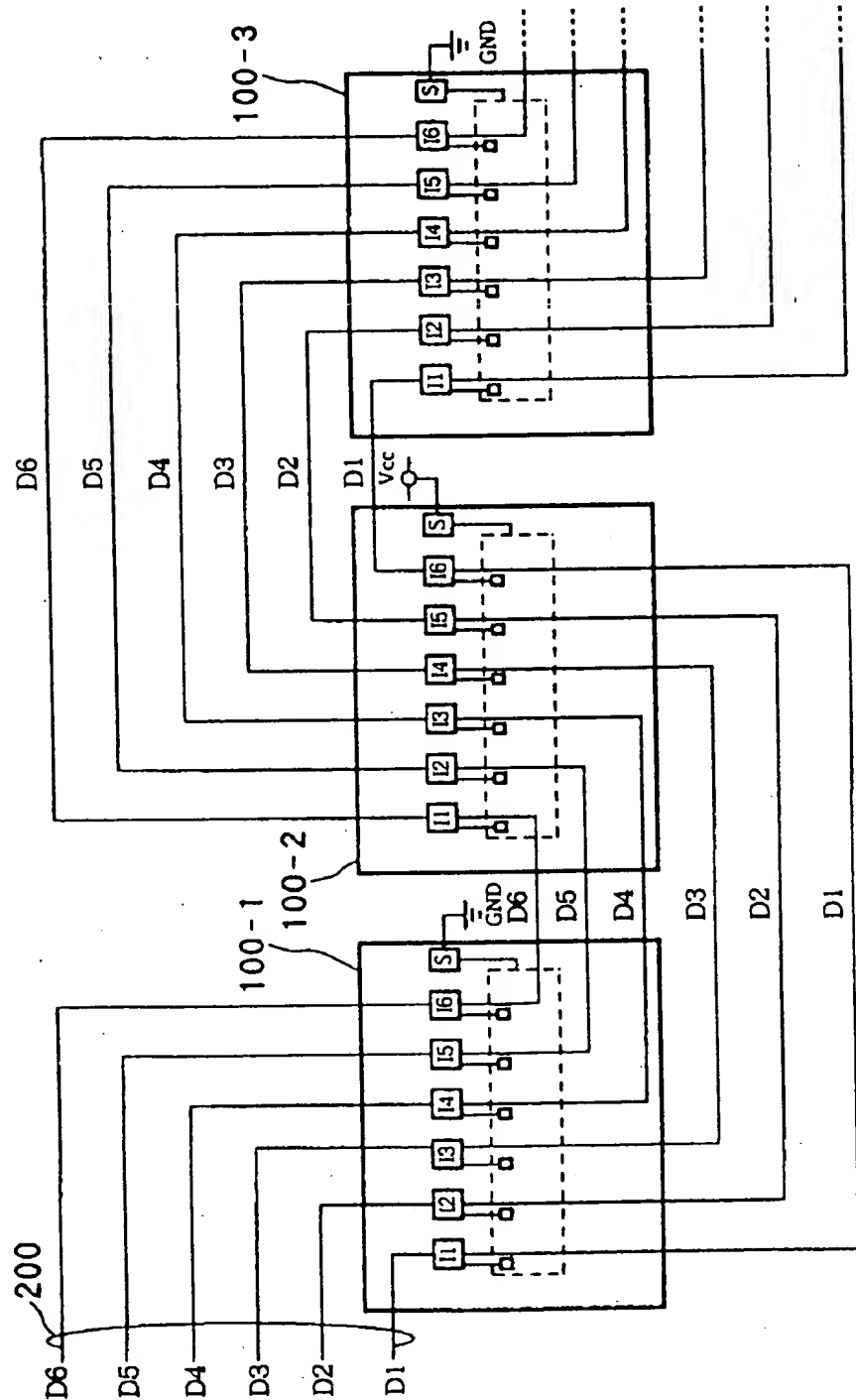


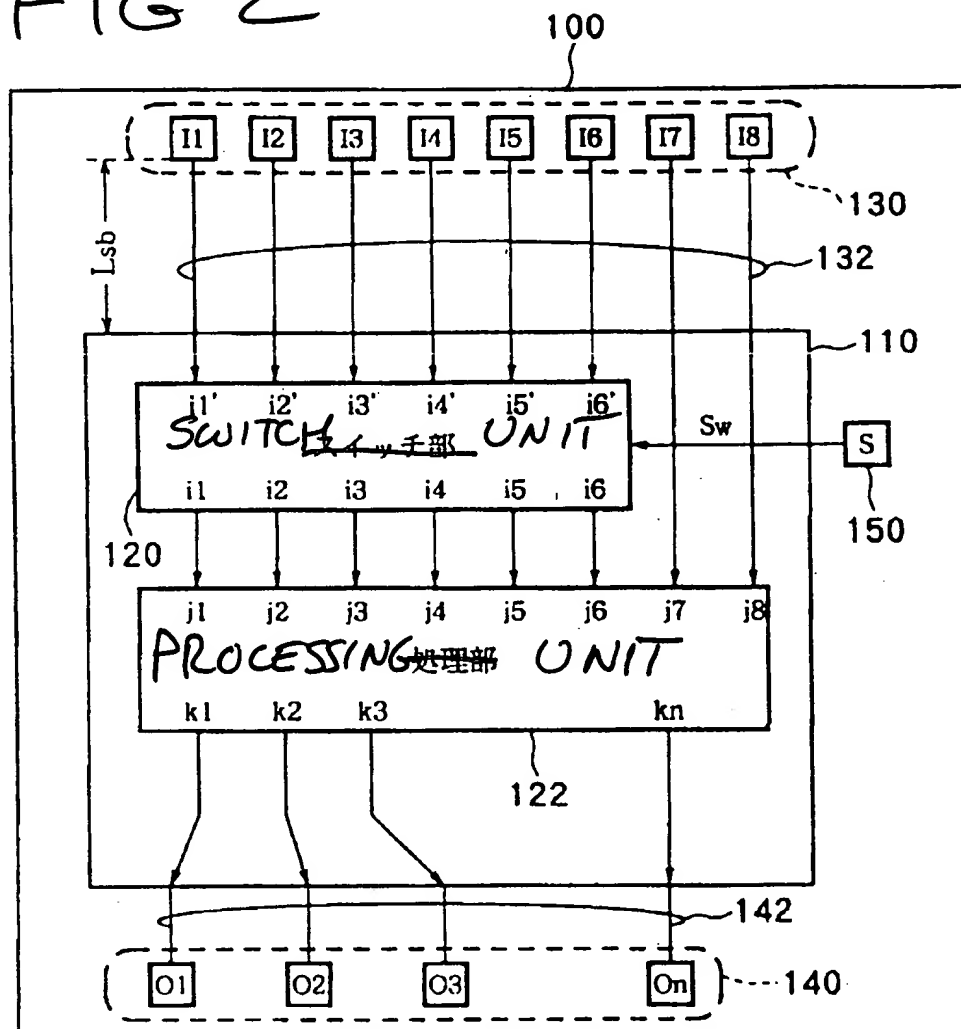
FIG. 1

整理番号 = 990515

(2)

【図2】

FIG 2



【図3】

Sw	INPUT	i1'	i2'	i3'	i4'	i5'	i6'
L	OUTPUT 出力	i1	i2	i3	i4	i5	i6
H		i6	i5	i4	i3	i2	i1

FIG 3

【図4】

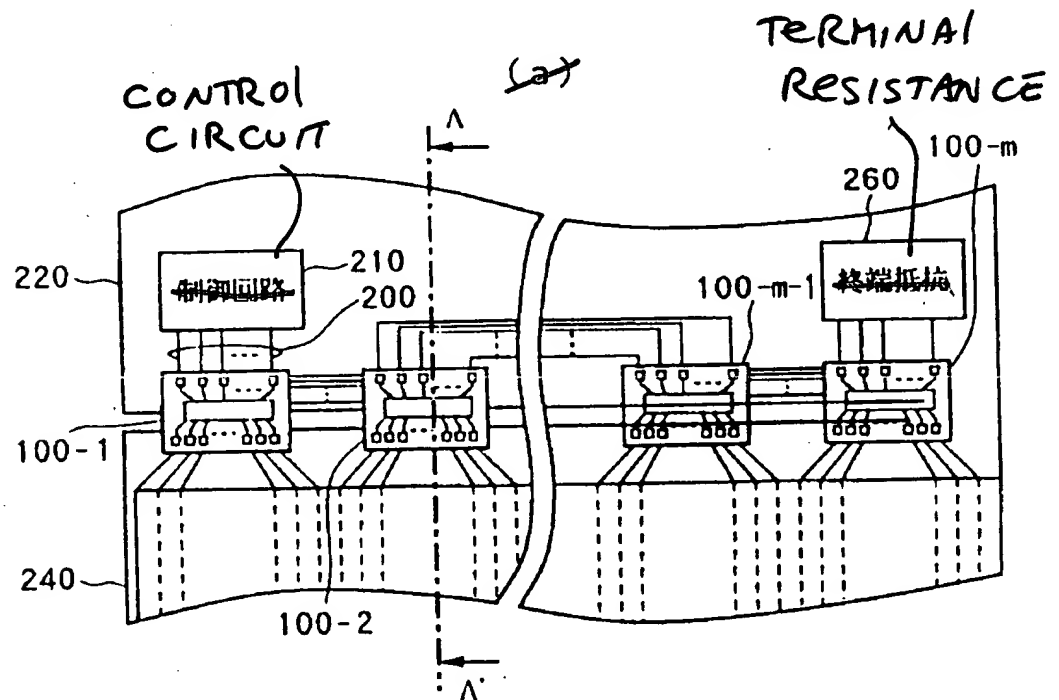


FIG 4A

(b)

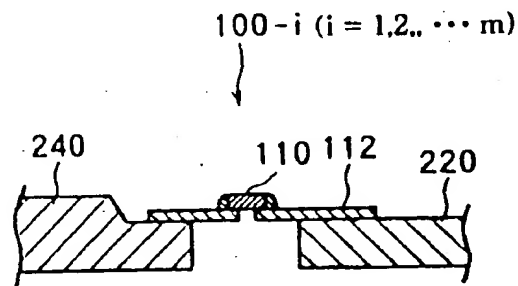


FIG 4B

【図5】

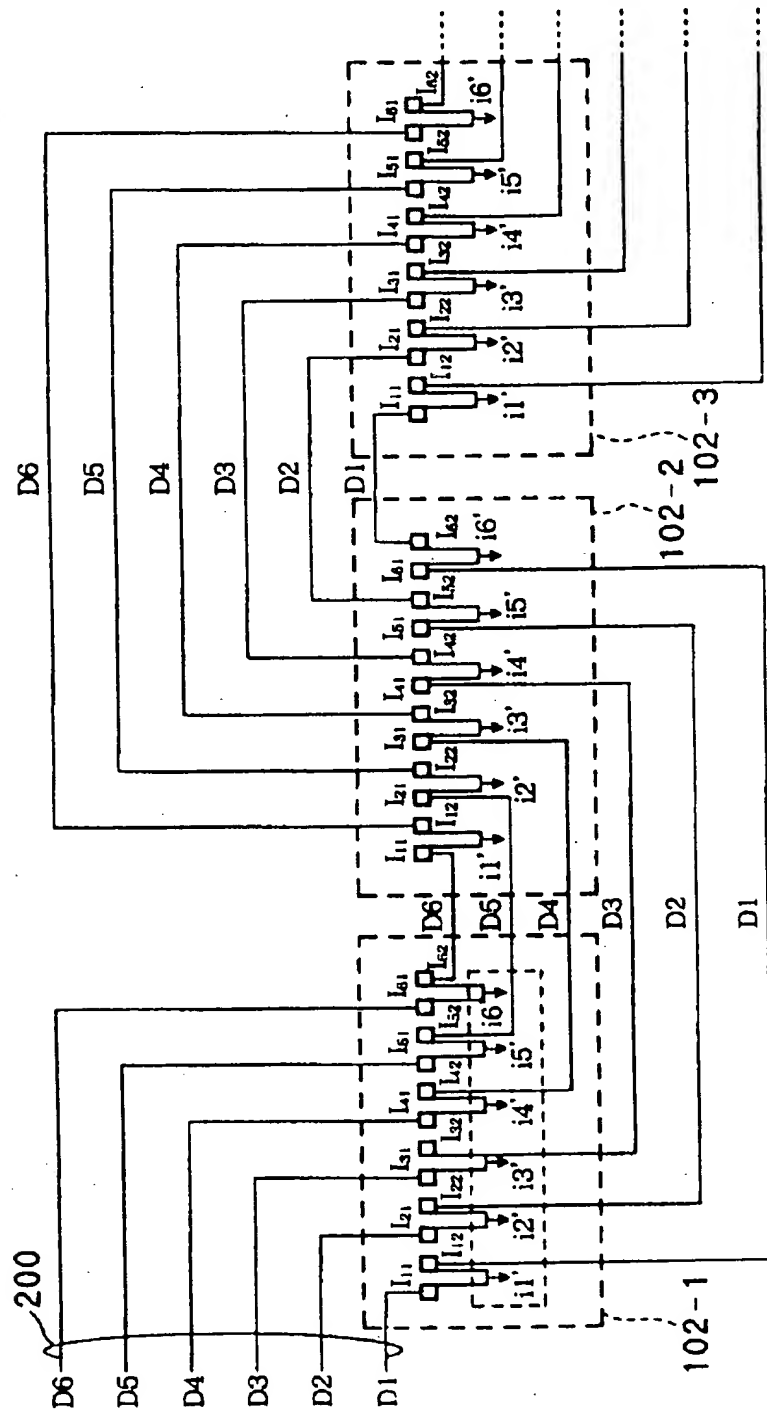


FIG. 5

【図6】

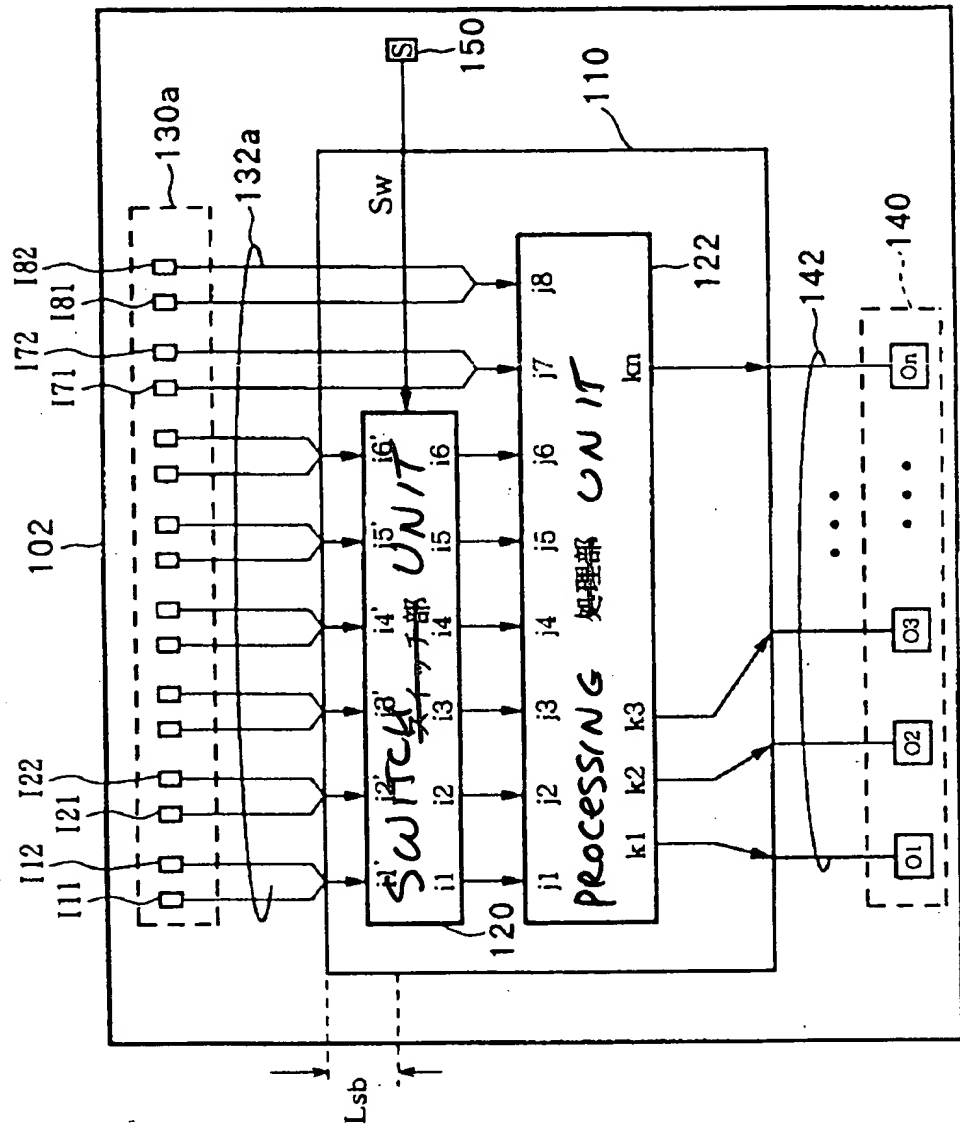


FIG. 6

整理番号 = 590



【図8】

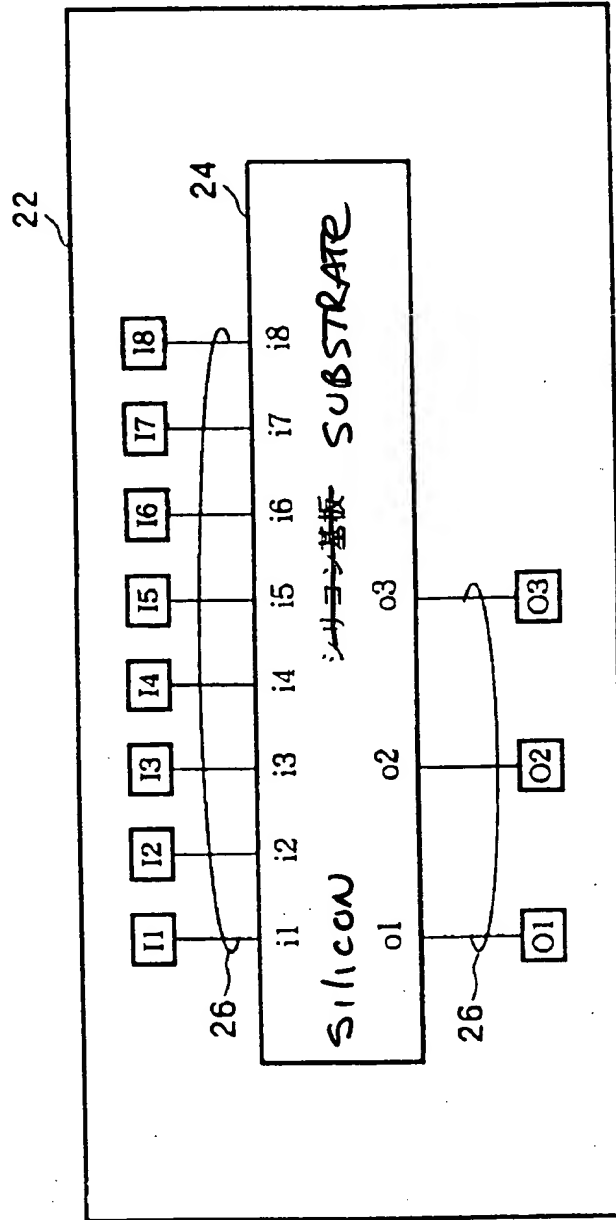


FIG. 8

【図9】

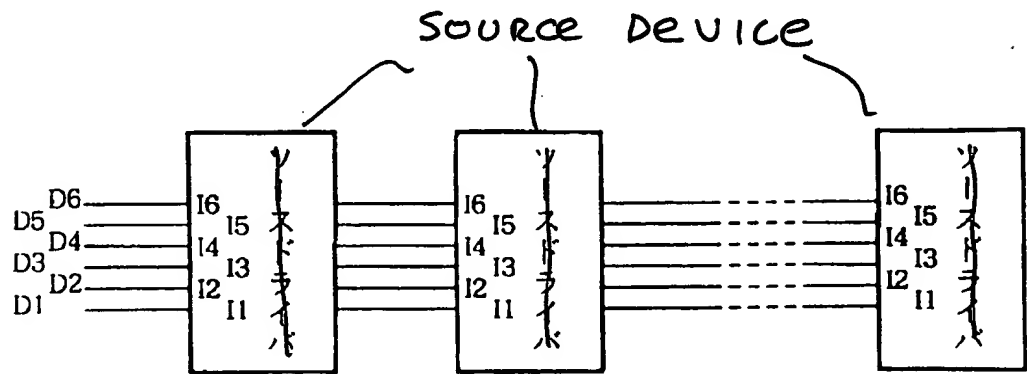
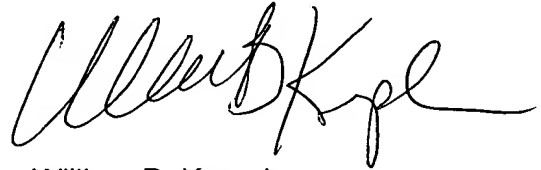


FIG. 9

REMARKS

The application and claims have been amended to place them in the appropriate form and to eliminate multiple claim dependencies. Early action on the merits is earnestly requested.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'William B. Kempler', with a stylized, flowing script.

William B. Kempler
Senior Corporate Patent Counsel
Reg. No.: 28,228

Texas Instruments Incorporated
P. O. Box 655474, MS 3999
Dallas, Texas 75265
(972) 917-5452
(972) 917-4407 (Fax)